

-	0	((multi adj process\$3) same exception same memory) and (process\$3 near3 (identif\$8))) and (vector near5 space)	USPAT	2003/11/13 18:04
-	8	((multi adj process\$3) same exception same memory) and (process\$3 near3 (identif\$8))) and (address near5 space)	USPAT	2003/11/13 18:04
-	5	((multi adj process\$3) same exception same memory) and (processor near3 (id or identification))	USPAT	2003/11/13 18:07
-	3667	(processor near3 (id or identification))	USPAT	2003/11/13 18:08
-	823	((processor near3 (id or identification))) and exception	USPAT	2003/11/13 18:08
-	191	(processor near3 (id or identification)).ab.	USPAT	2003/11/13 18:08
-	21	(processor near3 (id or identification)) and exception.ab.	USPAT	2003/11/13 18:18
-	80	(processor near3 (id or identif\$)) and exception.ab.	USPAT	2003/11/13 18:19
-	34	(processor near3 (id or identif\$)) and exception and (multi adj process\$).ab.	USPAT	2003/11/13 18:19
-	0	sanjay-lal.in.	USPAT	2003/11/13 18:24
-	4663	redback networks.as.	USPAT	2003/11/13 18:24
-	9	redback adj networks.as.	USPAT	2003/11/13 18:25
-	0	(redback adj networks.as.) and exception	USPAT	2003/11/13 18:25
-	6	(different near (operating adj system)) same (multi adj processor)	USPAT	2003/11/13 18:27
-	2	((different near (operating adj system)) same (multi adj processor)) and exception	USPAT	2003/11/13 18:26
-	115	(different near (operating adj system)) and (multi adj processor)	USPAT	2003/11/13 18:27
-	22	((different near (operating adj system)) and (multi adj processor)) and (handl\$3 near3 exception)	USPAT	2003/11/13 18:27
-	17	5606696.URPN.	USPAT	2003/11/13 18:31
-	9	5606696.URPN. and (multi adj processor)	USPAT	2003/11/13 18:32
-	4	(5606696.URPN. and (multi adj processor)) and exception	USPAT	2003/11/13 18:32
-	13	((multi adj processor) same interrupt same (processor near3 (id or identif\$8)))	USPAT	2003/11/13 18:37
-	547	multi adj processor and (interrupt near2 handl\$3)	USPAT	2003/11/14 18:17
-	481	(multi adj processor and (interrupt near2 handl\$3)) and register	USPAT	2003/11/14 17:52
-	36	((multi adj processor and (interrupt near2 handl\$3)) and register) and (exception near5 processor)	USPAT	2003/11/14 18:18
-	11	((multi adj processor and (interrupt near2 handl\$3)) and register) and (exception near5 processor)) and (common near5 interrupt)	USPAT	2003/11/14 18:18
-	1	common near5 (exception or interrupt) near5 (address adj space)	USPAT	2003/11/14 18:01
-	323	(exception or interrupt) near5 (address adj space)	USPAT	2003/11/14 17:57
-	21	((exception or interrupt) near5 (address adj space)) and (multi adj processor and (interrupt near2 handl\$3))	USPAT	2003/11/14 18:01
-	21	((exception or interrupt) near5 (address adj space)) and (multi adj processor and (interrupt near2 handl\$3))	USPAT; US-PGPUB; EPO; JPO	2003/11/14 18:01
-	3	common near5 (exception or interrupt) near5 (address adj space)	USPAT; US-PGPUB; EPO; JPO	2003/11/14 18:03

L Number	Hits	Search Text	DB	Time stamp
1	3590	(multi adj processor or multiprocessor) and exception and memory	USPAT; US-PGPUB; EPO; JPO	2003/11/20 20:14
6	412	((multi adj processor or multiprocessor) and exception and memory) and (register same vector same memory)	USPAT; US-PGPUB; EPO; JPO	2003/11/20 20:22
9	2	((multi adj processor or multiprocessor) and exception and memory) and (register same vector same memory same (processor near5 (id or ident\$9)))	USPAT; US-PGPUB; EPO; JPO	2003/11/20 20:19
8	185	((multi adj processor or multiprocessor) and exception and memory) and (register same vector same memory) and (processor near5 (id or ident\$9))	USPAT; US-PGPUB; EPO; JPO	2003/11/20 20:21
19	99	((multi adj processor or multiprocessor) and exception and memory) and (register same vector same memory) and ((processor near5 (id or ident\$9)) same register)	USPAT; US-PGPUB; EPO; JPO	2003/11/20 20:22
21	99		USPAT; US-PGPUB; EPO; JPO	2003/11/20 20:22
22	99		USPAT; US-PGPUB; EPO; JPO	2003/11/20 20:24
23	12		USPAT; US-PGPUB; EPO; JPO	2003/11/20 20:26
24	22	((multi adj processor or multiprocessor) and exception and memory) and (register same vector same memory) and (processor near5 handle\$5 near5 exception)	USPAT; US-PGPUB; EPO; JPO	2003/11/20 20:27
26	22		USPAT; US-PGPUB; EPO; JPO	2003/11/20 20:39
30	14	((dedicat\$5 near5 processor) same handl\$5 same exception)	USPAT; US-PGPUB; EPO; JPO	2003/11/20 20:47
31	11	((assigned near5 processor) same handl\$5 same exception)	USPAT; US-PGPUB; EPO; JPO	2003/11/20 20:52
33	11	((assigned adj2 processor) same exception)	USPAT; US-PGPUB; EPO; JPO	2003/11/20 20:53
34	19	((allocat\$3 adj2 processor) same exception)	USPAT; US-PGPUB; EPO; JPO	2003/11/20 20:55
35	18	((common near5 vector) same exception)	USPAT; US-PGPUB; EPO; JPO	2003/11/20 20:56
-	59	(multi adj process\$3) same exception	USPAT	2003/11/13 17:59
-	165	((device adj driver) same serial) same memory	USPAT	2003/11/13 17:59
-	43	(multi adj process\$3) same exception same memory	USPAT	2003/11/13 17:59
-	14	((multi adj process\$3) same exception same memory) and (process\$3 same (id or identification))	USPAT	2003/11/13 18:06
-	3	((multi adj process\$3) same exception same memory) and (process\$3 same (id or identification))) and (interrupt near2 handl\$3)	USPAT	2003/11/13 18:00
-	34	((multi adj process\$3) same exception same memory) and (process\$3 same (identif\$8))	USPAT	2003/11/13 18:02
-	17	((multi adj process\$3) same exception same memory) and (process\$3 near3 (identif\$8))	USPAT	2003/11/13 18:04

-	0	714/10-13.clas.	USPAT; US-PGPUB; EPO; JPO	2003/11/14 18:03
-	1169	714/10-13.ccls.	USPAT; US-PGPUB; EPO; JPO	2003/11/14 18:04
-	2337	712/10-31.ccls.	USPAT; US-PGPUB; EPO; JPO	2003/11/14 18:05
-	384	712/244.ccls.	USPAT; US-PGPUB; EPO; JPO	2003/11/14 18:05
-	0	714/10-13.ccls. and 712/10-31.ccls. and 712/244.ccls.	USPAT; US-PGPUB; EPO; JPO	2003/11/14 18:06
-	3820	714/10-13.ccls. or 712/10-31.ccls. or 712/244.ccls.	USPAT; US-PGPUB; EPO; JPO	2003/11/14 18:06
-	82	(714/10-13.ccls. or 712/10-31.ccls. or 712/244.ccls.) and (multi adj processor and (interrupt near2 handl\$3))	USPAT; US-PGPUB; EPO; JPO	2003/11/14 18:06
-	423	(714/10-13.ccls. or 712/10-31.ccls. or 712/244.ccls.) and (multi adj processor)	USPAT; US-PGPUB; EPO; JPO	2003/11/14 18:14
-	22	((714/10-13.ccls. or 712/10-31.ccls. or 712/244.ccls.) and (multi adj processor)) and (exception or interrupt).ab.	USPAT; US-PGPUB; EPO; JPO	2003/11/14 18:14
-	2	6272618.URPN.	USPAT	2003/11/14 18:09
-	1040	(714/10-13.ccls. or 712/10-31.ccls. or 712/244.ccls.) and ((multi adj processor) or multiprocessor)	USPAT; US-PGPUB; EPO; JPO	2003/11/14 18:14
-	9	((714/10-13.ccls. or 712/10-31.ccls. or 712/244.ccls.) and (multi adj processor)) and ((exception or interrupt) and (multiprocessor or (multi adj processor))).ab.	USPAT; US-PGPUB; EPO; JPO	2003/11/14 18:15
-	1124	(multi adj processor or multiprocessor) and (interrupt near2 handl\$3) and register	USPAT	2003/11/14 18:21
-	106	((multi adj processor or multiprocessor) and (interrupt near2 handl\$3) and register) and (exception near5 processor)	USPAT	2003/11/14 18:18
-	28	((multi adj processor or multiprocessor) and (interrupt near2 handl\$3) and register) and (exception near5 processor)) and (common near5 interrupt)	USPAT	2003/11/14 18:19
-	2	((714/10-13.ccls. or 712/10-31.ccls. or 712/244.ccls.) and ((multi adj processor) or multiprocessor)) and (common near5 interrupt near handler)	USPAT	2003/11/14 18:19
-	16	((multi adj processor or multiprocessor) and exception and memory).ab.	USPAT; US-PGPUB; EPO; JPO	2003/11/14 18:28
-	0	((multi adj processor or multiprocessor) and exception and memory and handl\$%).ab.	USPAT; US-PGPUB; EPO; JPO	2003/11/14 18:28
-	3	((multi adj processor or multiprocessor) and exception and memory and handl\$5).ab.	USPAT; US-PGPUB; EPO; JPO	2003/11/14 18:32
-	2969	((multi adj processor or multiprocessor) and exception and memory and handl\$5)	USPAT; US-PGPUB; EPO; JPO	2003/11/14 18:30
-	103	((multi adj processor or multiprocessor) same exception same memory)	USPAT; US-PGPUB; EPO; JPO	2003/11/14 18:32
-	27	((multi adj processor or multiprocessor) same exception same memory)) and (handl\$5 near5 exception)	USPAT; US-PGPUB; EPO; JPO	2003/11/14 18:31
-	19	((multi adj processor or multiprocessor) and interrupt and memory and handl\$5).ab.	USPAT; US-PGPUB; EPO; JPO	2003/11/14 18:39

-	16	((multi adj processor or multiprocessor) and exception and memory).ab.	USPAT; US-PGPUB; EPO; JPO	2003/11/14 18:45
-	103	((multi adj processor or multiprocessor) same exception same memory)	USPAT; US-PGPUB; EPO; JPO	2003/11/14 18:45
-	16	((multi adj processor or multiprocessor) same exception same memory)) and (memory same vector same address)	USPAT; US-PGPUB; EPO; JPO	2003/11/14 18:49
-	17	((multi adj processor or multiprocessor) same exception same memory)) and (identif\$5 near2 processor)	USPAT; US-PGPUB; EPO; JPO	2003/11/14 18:49